

*mark A"* 5  
What is claimed is:

1. A plasma display panel comprising:  
a pair of substrates;  
a discharge gas being filled in a gap between the substrates; and  
a mesh-patterned partition arranged on the inner surface of one of the substrates for dividing the gap into plural squares corresponding to a cell arrangement, wherein the partition is a structure having low portions lowered for forming a mesh-like air path that travels all gas-filled space enclosed by the partition in a plan view.  
10
2. The plasma display panel according to claim 1, wherein the difference between the heights of the upper surface of the partition is more than 5% of the maximum height.  
15
3. The plasma display panel according to claim 1, wherein the difference between the heights of the upper surface of the partition is more than 10  $\mu\text{m}$ .
4. The plasma display panel according to claim 1,  
20 wherein a fluorescent material is arranged on the row direction side and the column direction side of the partition in each cell that constitutes the display surface.
5. The plasma display panel according to claim 1, wherein the plan view pattern of the partition is a check pattern  
25 that divides the gap into cells in the row direction and in the column direction of the matrix display and an inter-row portion of the partition that is a boundary wall between rows is lower than other portions.
6. The plasma display panel according to claim 5,  
30 wherein the inter-row portion has a plan view pattern enclosing

*Conceded*  
at least one space for each column.

7. The plasma display panel according to claim 6, wherein the plan view pattern of the inter-row portion is a ladder pattern.

5        8. The plasma display panel according to claim 5, wherein the partition is arranged on the back substrate, an electrode including a transparent conductive film and a metal film straddling over all columns is arranged on the front substrate, and the metal film and the inter-row portion are overlaid in the plan view.

10      9. The plasma display panel according to claim 1, wherein the partition is a baked material having a heat shrink property, and the width of the low portions of the partition is wider than that of the other portion of the partition.

15      10. A method for manufacturing a plasma display panel according to claim 1, the method comprising the steps of:

forming a layer made of a partition material having a heat shrink property on a substrate;

20      patterning the layer to be a mesh pattern having a large pattern width portion at the ring-shaped pattern enclosing a cell in the plan view; and

forming the partition by baking the patterned layer.

25      11. The method according to claim 10, wherein the patterning step includes the steps of placing a cutting mask corresponding to the mesh pattern on the layer, and cutting non-masked portions of the layer by sandblasting.

*Add*  
*A12*